

AR0822_MPBGA75_Demo3Head_SER

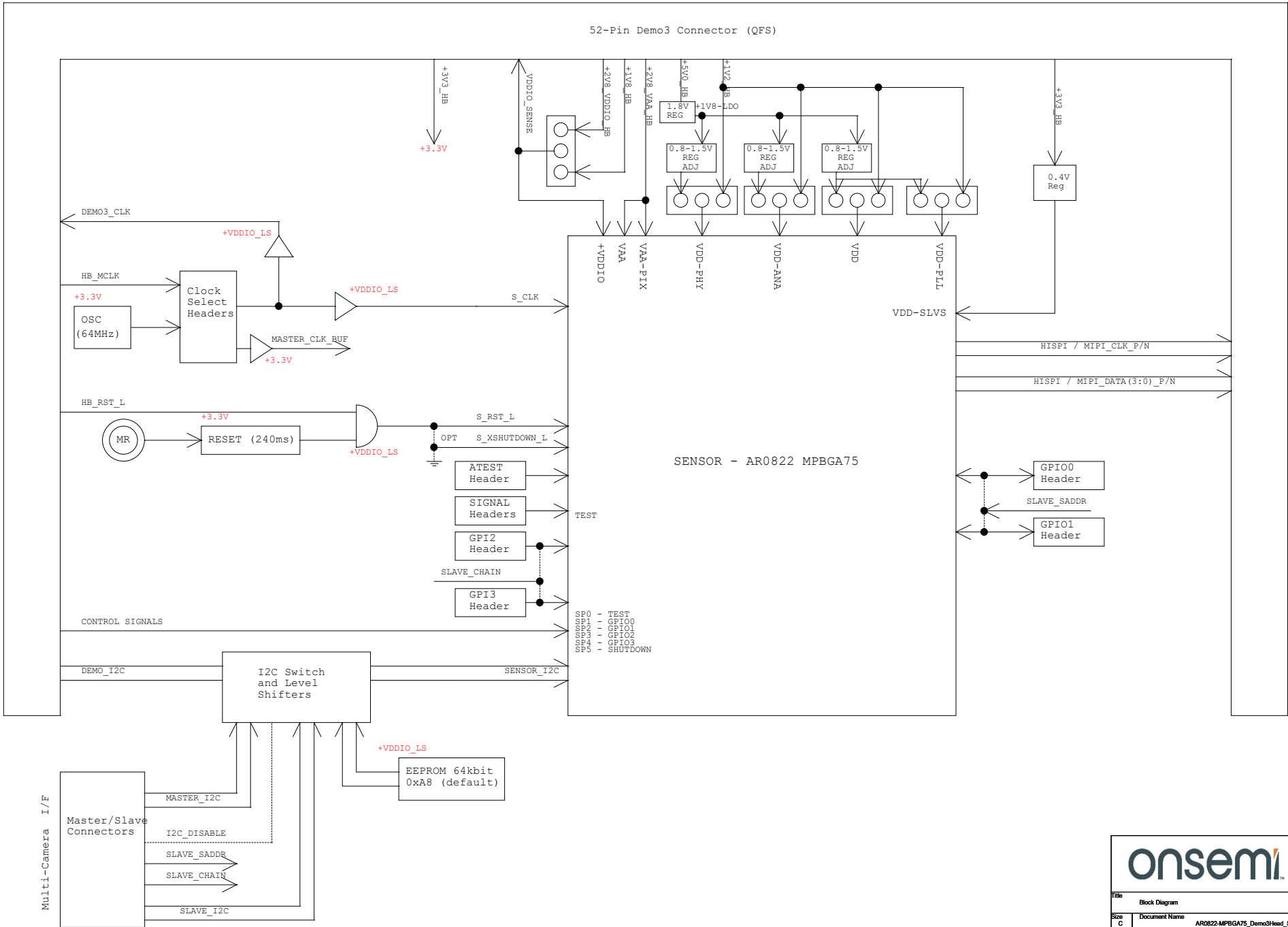
Page	Description
1	Title Page
2	Block Diagram
3	Sensor
4	Power
5	Clock and Reset
6	External Interfaces

Rev	Who	Date	Description
Rev 0.0	anahar	26MAR2020 05MAR2021 23APR2021 16FEB2023	Reference from AR0822_GCSP_DEMO3HEAD_SER Updated P20 setting as per AE feedback from 1-3 to 3-5. Added text to tune R21, R24 and R25 as per AE feedback onsemi logo updated



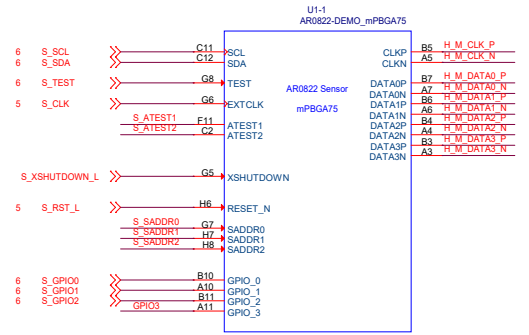
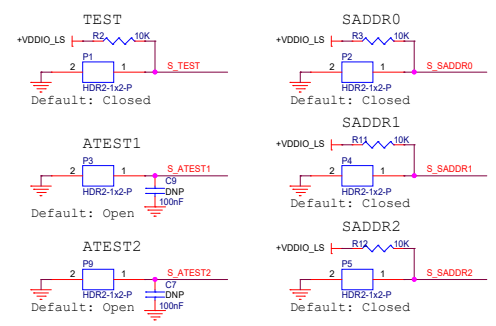
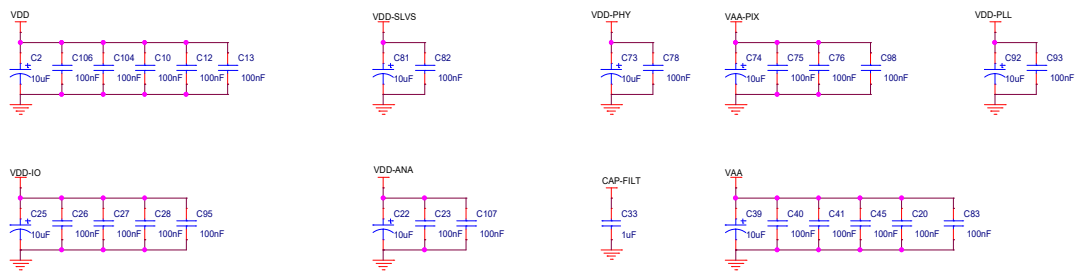
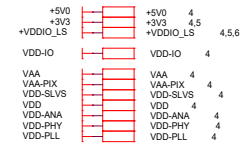
Title Page		
Size	Document Name	Rev
C	AR0822-MPBGA75_Demo3Head_SER	0.0
Date:	Thursday, February 16, 2023	Sheet 1 of 6

Block Diagram

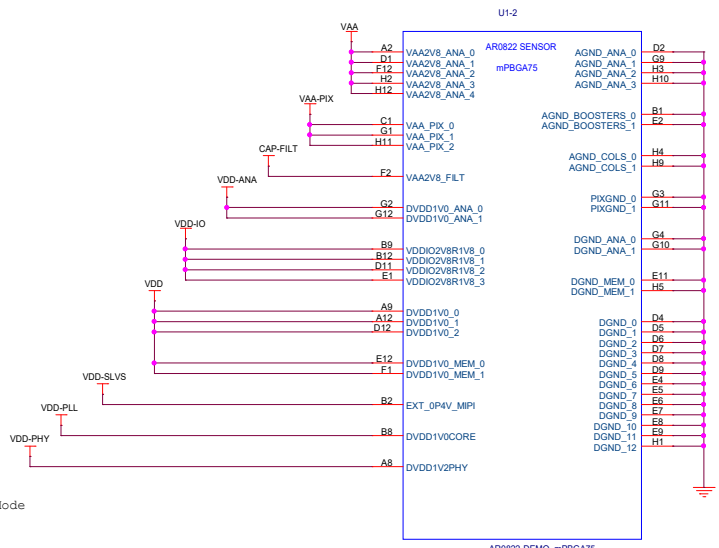
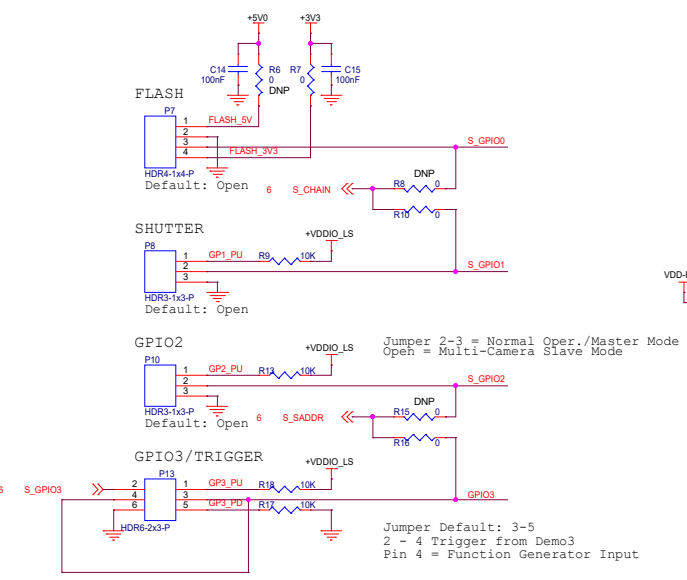
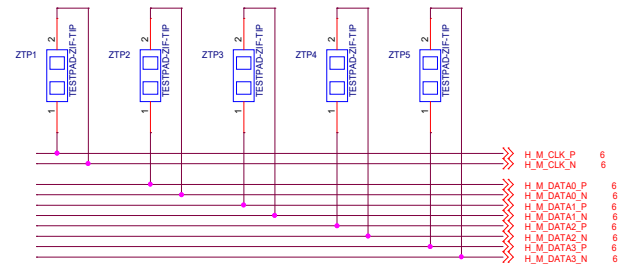


File		
Block Diagram		
Size	Document Name	Rev
C	AR0822-MPBGA75_Demo3Head_SER	0.0
Date:	Thursday, February 16, 2023	Sheet 2 of 6

Sensor



(Note for layout: - Place these testpads near the Demo3 I/F connector at the top side of PCB)



SIGNAL	GPIO FUNCTION OPTIONS
GPIO0	a. Flash output (default) b. All options in GPIO2 (if use as input)
GPIO1	a. Shutter output (default) b. 3D daisy chain communication output c. All options in GPIO2 (if use as input)
GPIO2	a. SADDR, second I2C device address b. Trigger signal for Slave Mode c. Standby
GPIO3	a. 3D daisy chain communication input b. All options in GPIO2

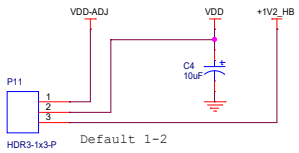
File: Sensor
Size: Document Name AR0822-MPBG75_Demo3Head_SER Rev 0.0
Date: Thursday, February 16, 2023 **Sheet:** 3 of 6

Script executed

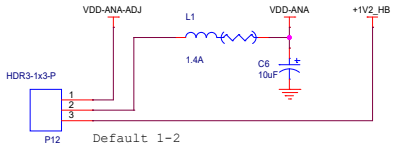
Debug Headers: Cut away the shortened trace and mount header for power debugging

Power

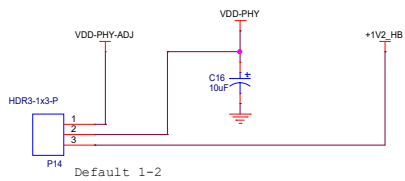
VDD SUPPLY



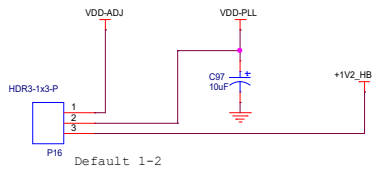
VDD-ANA SUPPLY



VDD-PHY SUPPLY

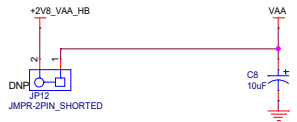


VDD-PLL 1.2V SUPPLY



Note :- Voltage of VDD, VDD-ANA, VDD-PHY and VDD-PLL need to be tuned to 1.1V for Rev. 2.0 silicon and 1.05V for Rev. 2.1 silicon.

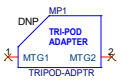
VAA 2.8V SUPPLY



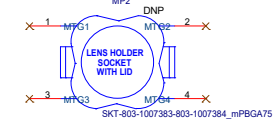
VAA-PIX 2.8V SUPPLY



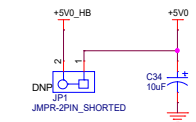
Tripod Mount



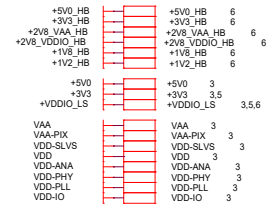
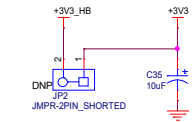
Socket/Lens Mount



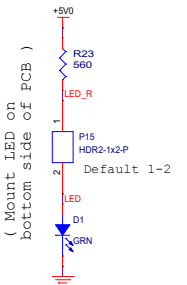
PERIPHERAL 5.0V SUPPLY



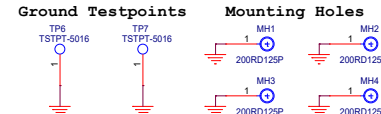
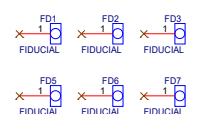
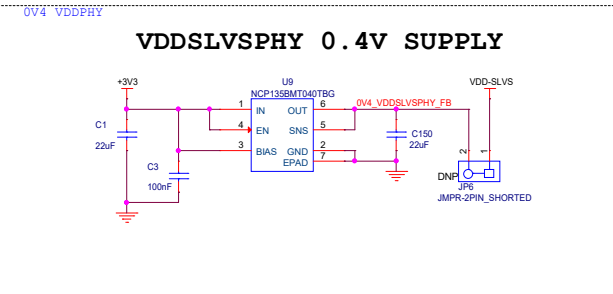
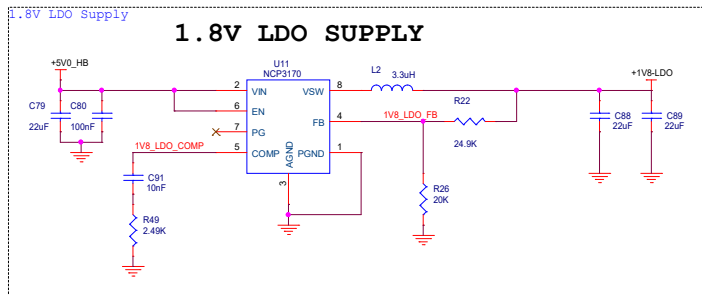
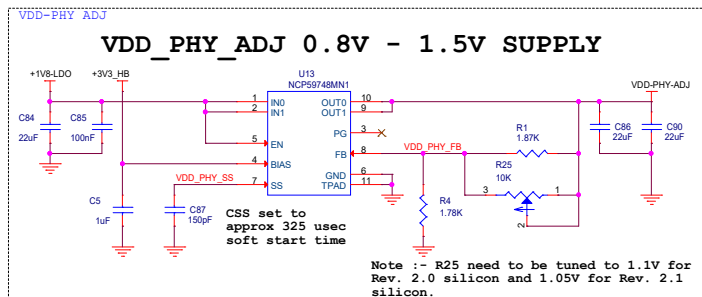
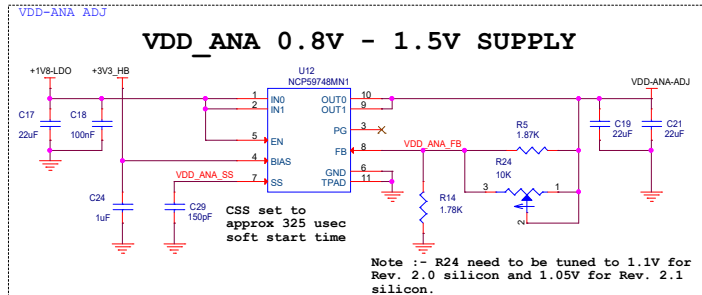
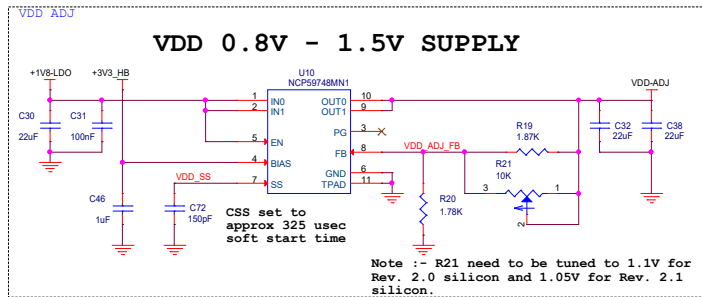
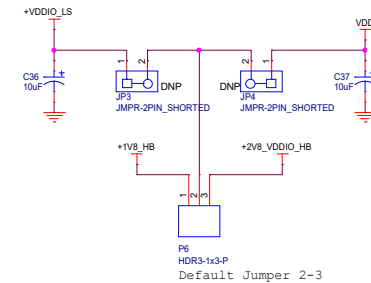
PERIPHERAL 3.3V SUPPLY



5V LED



VDD-IO & VDDIO_LS 2.8V SUPPLY

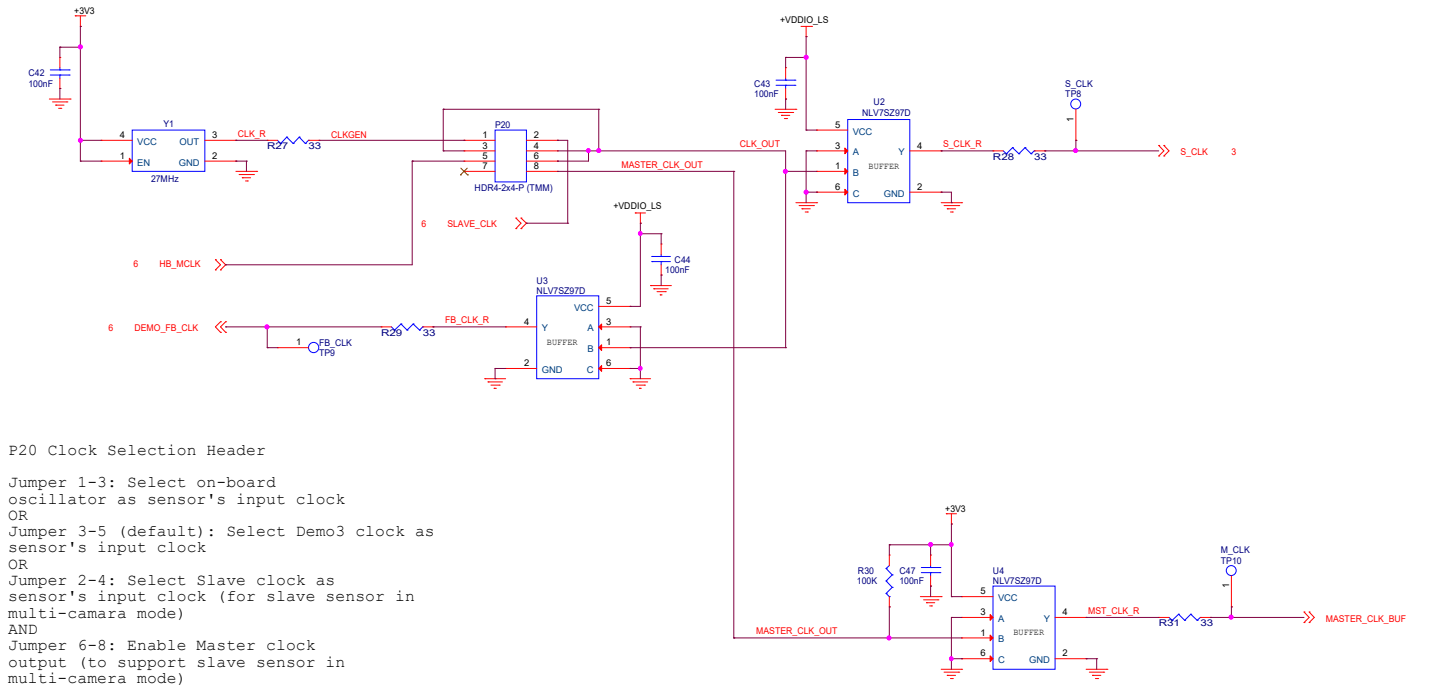


File	Power	Rev	0.0
Size	Document Name	AR0822-MPBG75_Demo3Head_SER	
C	Date:	Thursday, February 16, 2023	Sheet 4 of 6

Clock and Reset

+5V0 3.4
 +3V3 3.4
 +VDDIO_LS 3.4.6

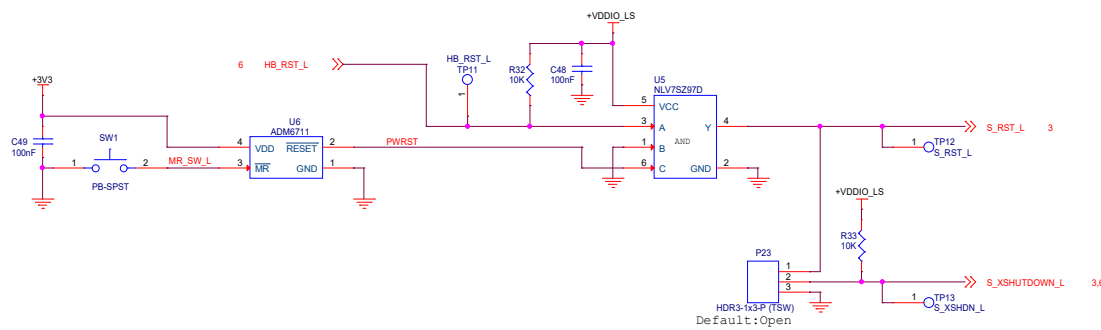
CLOCK CIRCUIT



P20 Clock Selection Header

- Jumper 1-3: Select on-board oscillator as sensor's input clock
- OR
- Jumper 3-5 (default): Select Demo3 clock as sensor's input clock
- OR
- Jumper 2-4: Select Slave clock as sensor's input clock (for slave sensor in multi-camera mode)
- AND
- Jumper 6-8: Enable Master clock output (to support slave sensor in multi-camera mode)

RESET CIRCUIT



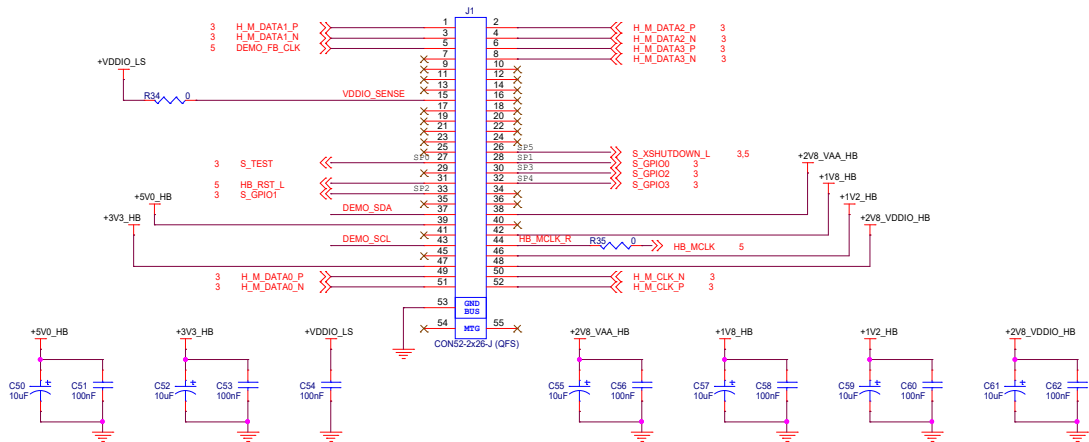
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Title		
Clock and Reset		
Size	Document Name	Rev
C	AR0822-MPBGA75_Demo3Head_SER	0.0
Date:	Thursday, February 16, 2023	Sheet 5 of 6

External Interface

+5V0_HB	4	+5V0_HB	4
+3V3_HB	4	+3V3_HB	4
+2V8_VAA_HB	4	+2V8_VAA_HB	4
+2V8_VDDIO_HB	4	+2V8_VDDIO_HB	4
+1V8_HB	4	+1V8_HB	4
+1V2_HB	4	+1V2_HB	4
+3V3	3,4,5	+3V3	3,4,5
+VDDIO_LS	3,4,5	+VDDIO_LS	3,4,5

DEMO3 BASEBOARD I/F

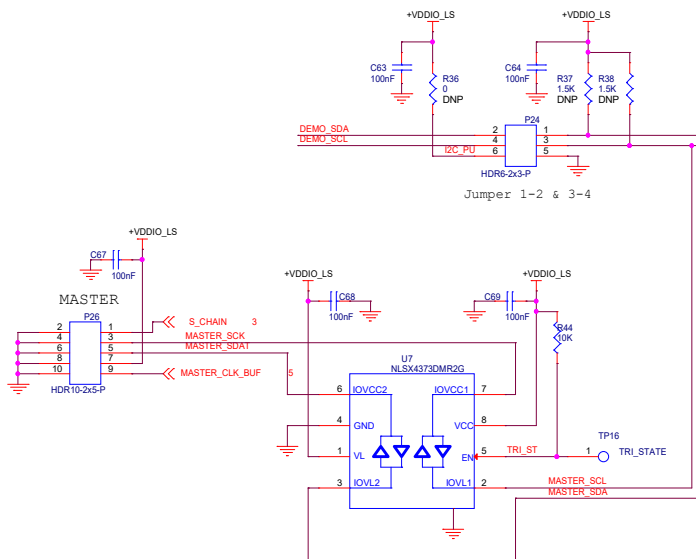


MULTI-CAMERA INTERFACE

MASTER / SLAVE Connection in Multi-Camera Mode:

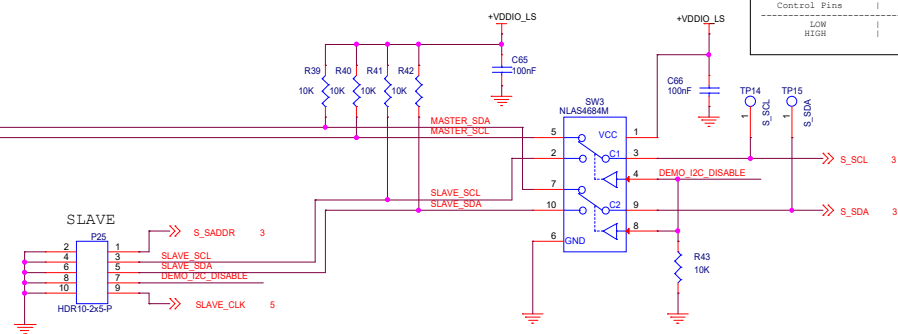
- Connect a multi-camera interface cable from the MASTER connector on the Master headboard to the SLAVE connector on the Slave headboard
- If there is a further Slave headboard down the chain, connect another multi-camera interface cable from the MASTER connector on the 1st Slave headboard to the SLAVE connector on the 2nd Slave headboard

I2C DEBUG

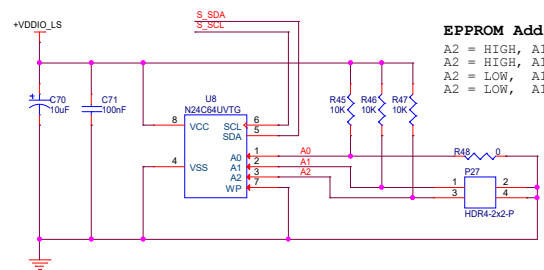


I2C SWITCH

SW3 Function Table		
Control Pins		On Channel
LOW		Demo I2C Enabled
HIGH		Slave I2C Enabled



LENS CORRECTION EEPROM



EPPROM Address Switch Settings:
 A2 = HIGH, A1 = LOW, A0 = LOW; Address => 0xA8 (default)
 A2 = HIGH, A1 = HIGH, A0 = LOW; Address => 0xAC
 A2 = LOW, A1 = HIGH, A0 = LOW; Address => 0xA4
 A2 = LOW, A1 = LOW, A0 = LOW; Address => 0xA0

Title	External Interface	
Size	Document Name	Rev
C	AR0822-MPBGA75_Demo3Head_SER	0.0
Date	Thursday, February 16, 2023	Sheet 6 of 6