

SPLD PAL Family 8 Macro Cells 62.5MHz 5V 20-Pin PLCC



Images are for reference only

[Inquiry](#)

Manufacturer: [Lattice Semiconductor Corp](#)

Package/Case: PLCC20

Product Type: Programmable Logic ICs

Lifecycle: Obsolete

General Description

The PALCE16V8 is an advanced PAL device built with low-power, high-speed, electricallyerasable CMOS technology. It is functionally compatible with all 20-pin GAL devices. The macrocells provide a universal device architecture. The PALCE16V8 will directly replace the PAL16R8, with the exception of the PAL16C1.

The PALCE16V8Z provides zero standby power and high speed. At 30- μ A maximum standby current, the PALCE16V8Z allows battery-powered operation for an extended period.

The PALCE16V8 utilizes the familiar sum-of-products (AND/OR) architecture that allows users to implement complex logic functions easily and efficiently. Multiple levels of combinatorial logic can always be reduced to sum-of-products form, taking advantage of the very wide input gates available in PAL devices. The equations are programmed into the device through floating-gate cells in the AND logic array that can be erased electrically.

The fixed OR array allows up to eight data product terms per output for logic functions. The sum of these products feeds the output macrocell. Each macrocell can be programmed as registered or combinatorial with an active-high or active-low output. The output configuration is determined by two global bits and one local bit controlling four multiplexers in each macrocell.



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PLCC28

PALCE24V10H-15PC

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PLCC28

PALCE20V8Q-20JI/4

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BGA

PALCE24V10H-25JC

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