


## Analog Video 200MHz 16 x 16 100-Pin LQFP Tray

<b>Manufacturer:</b>	<u>Analog Devices, Inc</u>
<b>Package/Case:</b>	QFP
<b>Product Type:</b>	Switches
<b>RoHS:</b>	RoHS Compliant/Lead free 
<b>Lifecycle:</b>	Active



Images are for reference only

[Inquiry](#)

## General Description

### Key Features

### Application

16 × 16 high speed nonblocking switch arrays: G = 2

The differential gain and differential phase of better than 0.05% and 0.05°, respectively, along with a 0.1 dB flatness out to 25 MHz while driving a 75 Ω back-terminated load, make the AD8114/AD8115 ideal for all types of signal switching.

Serial or parallel programming of switch array

The AD8114/AD8115 include 16 independent output buffers that can be placed into a high impedance state for paralleling crosspoint outputs so that off channels do not load the output bus. The AD8114 has a gain of 1, while the AD8115 offers a gain of 2. They operate on voltage supplies of ±5 V while consuming only 70 mA of idle current. The channel switching is performed via a serial digital control (which can accommodate daisy-chaining of several devices) or via a parallel control, allowing updating of an individual output without reprogramming the entire array.

Serial data out allows daisy-chaining of multiple 16 × 16 arrays to create larger switch arrays

The AD8114/AD8115 is packaged in a 100-lead LQFP and is available over the extended industrial temperature range of -40°C to +85°C.

High impedance output disable allows connection of

### Applications

multiple devices without loading the output bus

Routing of high speed signals, including

For smaller arrays see the AD8108/AD8109 (8 × 8) or AD8110/AD8111 (16 × 8) switch arrays

Complete solution

Buffered inputs

Programmable high impedance outputs

16 output amplifiers (G = 2)

Drives 150 Ω loads

Excellent video performance

25 MHz, 0.1 dB gain flatness

0.05%/0.05° differential gain/differential phase error (R)

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Excellent ac performance

Slew rate: 375 V/μs

Low power of 700 mW (2.75 mW per point)

Low all hostile crosstalk of 70 dB at 5 MHz

Reset pin allows disabling of all outputs (connected through a capacitor to ground provides power-on reset capability)

100-lead LQFP (14 mm × 14 mm)



## Recommended For You

### AD1803JRU

Analog Devices, Inc

TSSOP24

### AD1847JP

Analog Devices, Inc

PLCC

### AD8109ASTZ

Analog Devices, Inc

QFP

**AD1980JST-REEL**

Analog Devices, Inc  
QFP48

**AD1836AAS**

Analog Devices, Inc  
QFP52

**AD1843JS**

Analog Devices, Inc  
QFP

**AD1888JCPZ-REEL**

Analog Devices, Inc  
LFCSP-48

**AD8116JSTZ**

Analog Devices, Inc  
QFP128

**ADV601LCJST**

Analog Devices, Inc  
QFP

**ADV611JST**

Analog Devices, Inc  
QFP

**ADN4600ACPZ**

Analog Devices, Inc  
QFN

**AD8152JBPZ**

Analog Devices, Inc  
BGA

**ADN4605ABPZ**

Analog Devices, Inc  
BGA

**AD8113JSTZ**

Analog Devices, Inc  
QFP

**ADN4612ACPZ**

Analog Devices, Inc  
LFCSP-88