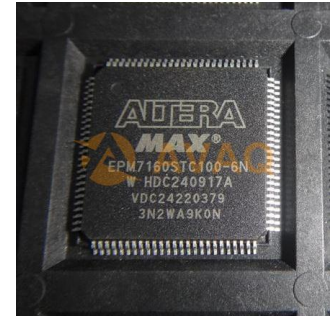


CPLD MAX® 7000S Family 3.2K Gates 160 Macro Cells
149.3MHz 5V 100-Pin TQFP Tray



Images are for reference only

[Inquiry](#)

Manufacturer: [Intel Corp](#)

Package/Case: TQFP100

Product Type: Programmable Logic ICs

RoHS: RoHS Compliant/Lead free 

Lifecycle: Obsolete

General Description

EPM7160STC100-6N is an FPGA (Field-Programmable Gate Array) device from Intel Corporation (formerly known as Altera).

Key Features

The device belongs to the MAX 7000S series and has a density of 60,000 usable gates.

It has 100 pins and is housed in a surface-mount technology (SMT) package with a size of 14 x 14 mm.

The device operates with a supply voltage of 4.5V to 5.5V and has a maximum operating frequency of 250 MHz.

The EPM7160STC100-6N has 6 ns propagation delay for its combinational logic.

Application

The device can be used in various applications such as telecommunications, industrial automation, consumer electronics, and automotive.

It can be used for implementing complex digital logic circuits that require high-speed operation, low power consumption, and high reliability.



Recommended For You

EPMB256AQC208-10N

Intel Corp

QFP208

EPCQ32ASI8N

Intel Corp

SOP8

EPCQ32SI8N

Intel Corp

SOP8

EPCQ64ASI16N

Intel Corp

SOP16

EPCQ16SI8N

Intel Corp

SOP8

EPC21I32

Intel Corp

QFP

EPM7128STC100-15N

Intel Corp

QFP100

EP1C6Q240I7N

Intel Corp

QFP240

EPCQ128SI16N

Intel Corp

SOP16

EPM7128SLC84-15N

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PLCC

EPC1213PC8

Intel Corp

DIP8

EP1K30TC144-3N

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QFP

EPCS1SI8

Intel Corp

SOP-8

EPC1PI8N

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DIP8

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