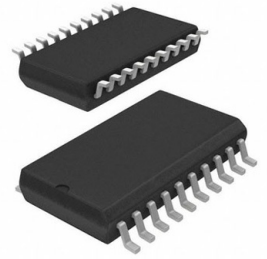


## Flip Flop D-Type Bus Interface Pos-Edge Open Drain 1-Element Automotive 20-Pin SOIC Tube



Images are for reference only

**Manufacturer:** [Texas Instruments, Inc](#)

**Package/Case:** SOP20

**Product Type:** Logic ICs

**RoHS:** RoHS Compliant/Lead free 

**Lifecycle:** Active

[Inquiry](#)

### General Description

The TPIC6B273 is a monolithic, high-voltage, medium-current, power logic octal D-type latch with DMOS-transistor outputs designed for use in systems that require relatively high load power. The device contains a built-in voltage clamp on the outputs for inductive transient protection. Power driver applications include relays, solenoids, and other medium-current or high-voltage loads.

The TPIC6B273 contains eight positive-edge-triggered D-type flip-flops with a direct clear input. Each flip-flop features an open-drain power DMOS-transistor output.

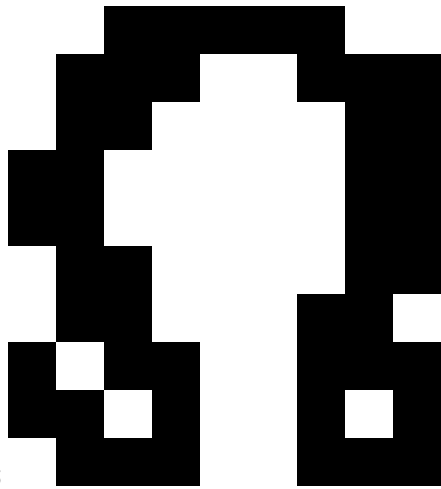
When clear (CLR) is high, information at the D inputs meeting the setup time requirements is transferred to the DRAIN outputs on the positive-going edge of the clock (CLK) pulse. Clock triggering occurs at a particular voltage level and is not directly related to the transition time of the positive-going pulse. When the clock input (CLK) is at either the high or low level, the D input signal has no effect at the output. An asynchronous CLR is provided to turn all eight DMOS-transistor outputs off. When data is low for a given output, the DMOS-transistor output is off. When data is high, the DMOS-transistor output has sink-current capability.

Outputs are low-side, open-drain DMOS

transistors with output ratings of 50 V and 150-mA continuous sink-current capability. Each output provides a 500-mA typical current limit at  $T_C = 25^\circ\text{C}$ . The current limit decreases as the junction temperature increases for additional device protection.

The TPIC6B273 is characterized for operation over the operating case temperature range of  $-40^\circ\text{C}$  to  $125^\circ\text{C}$ .

## Key Features



Low  $r_{DS(on)}$ ...5

Typical

Avalanche Energy ...30 mJ

Eight Power DMOS-Transistor Outputs of  
150-mA Continuous Current

500-mA Typical Current-Limiting Capability

Output Clamp Voltage...50 V

Low Power Consumption

## Recommended For You

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### TPIC6C595DR

Texas Instruments, Inc

SOP16

### CY74FCT16841CTPVC

Texas Instruments, Inc

SSOP56

### TPIC6C595DG4

Texas Instruments, Inc

SOIC-16

### TPIC6B259N

Texas Instruments, Inc

DIP20

### TPIC6A259NE

Texas Instruments, Inc

DIP

### TPIC6A596NE

Texas Instruments, Inc

DIP

### TPIC6259N

Texas Instruments, Inc

DIP20

### TPIC6C596N

Texas Instruments, Inc

PDIP-16

### TPIC6273N

Texas Instruments, Inc

DIP20

### TPIC6C595N

Texas Instruments, Inc

DIP-16

### TPIC6595DW

Texas Instruments, Inc

SOP20

### TPIC6A259DW

Texas Instruments, Inc

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**TPIC6C596D**

Texas Instruments, Inc

SOP-16

**TPIC6C595DRG4**

Texas Instruments, Inc

SOP16

**TPIC6A595NE**

Texas Instruments, Inc

PDIP-20